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MOTIVATION

The Current State of DSP Programming Why Yet Another Programing Language?

Kronos – an Overview

A Language Specification A Just-in-Time Compiler Type Determinism

CASE STUDIES

Examples



Research background: PWGL



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- Doctoral study project since 2010



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- High performance programs are low level
 - Many powerful abstractions have performance penalties
 - Tedious to write for professionals



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Many of us here are working on a subset of these problems. The final solution is not yet here.



KRONOS an Overview



A LANGUAGE SPECIFICATION SYNTAX



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- Algebraic data structure yields pairs, lists and trees list = (a b c d)
- Tie-in allows for partial decomposition too (first-element other-elements) = list



A LANGUAGE SPECIFICATION CLASSIFICATION



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Functional Programming



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 - Implicit inferral of control and audio signals



Listing 1: Fold, a higher order function for reducing lists with example replies.

```
Fold(folding-function x)
{
   Fold = x
}

Fold(folding-function x xs)
{
   Fold = Eval(folding-function x Fold(folding-function xs))
}

/* Add several numbers */
Fold(Add 1 2 3 4) => 10
/* Multiply several numbers */
Fold(Mul 5 6 10) => 300
```





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 - Some enhancements to SoftWire, a LGPL dynamic assembler written by Nicolas Capens
- Programs are configured on the fly for the present I/O configuration
- Good runtime performance using SSE4.2
 - Comparable and often superior to an optimizing C-compiler



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 - Minimal branching!
- No good for writing a word processor
- Designed for DSP inner loops



CASE STUDIES



SIGNAL GENERATION

- Delay is a first-class unary operator
- Recursion permitted via delays
- Recursion can be turned into an osc by clocking a section of the loop
 - IO:Audio-Gen(sig) provides signal updates at the audio rate

Listing 2: A Simple Phasor Oscillator

```
Phasor (freq)
  next-phase = IO:Audio-Gen(z-1('0 phase + freq))
  phase = next-phase - Truncate(next-phase)
  Phasor = phase + phase - 1
```



• Source is the math definition of series approximation!

Listing 3: Taylor-series Cosine

```
Sine-Coef(n)
{
    Sine-Coef = Crt:pow(Pi n * #2 - #1) * Crt:pow(#-1 n + #1) / Factorial(n * #2 - #1)
}
Fast-Cos(x order)
{
    Use Algorithm
    xm = Abs(x)
    xp = xm - 0.5
    coefs = Map(Sine-Coef Count-To(order))
    Fast-Cos = xp * Horner-Polynomial(xp * xp Reverse(coefs))
}
```



- Combining the $-\cos(\pi x)$ mapper and the phasor, a sine oscillator is created
- With higher order functions, oscillator banks can be constructed from a list of frequencies!

Listing 4: Sinusoid oscillation by mapping the phasor

```
FSin(freq)
{
   FSin = Fast-Cos(Phasor(freq) #8)
}
/* Example of using higher order functions */
Osc-Bank = Reduce(Add Map(FSin freq1 freq2 freq3 freq4))
```



SIN-OSC APPLICATIONS

Additive and FM Synthesis are easily constructed

Listing 5: Sinusoid Synthesis

```
Suppress-Alias(f0 amp) {Suppress-Alias = (f0 < 0.4) & amp}
Additive(f0 num-harmonics harmonic-coef harmonic-spread)
  Use Algorithm
  freqs = Map(Curry(Mul f0) Expand(num-harmonics Curry(Add harmonic-spread) 1))
  amps = Expand(num-harmonics Curry(Mul harmonic-coef) 1)
  oscs = Zip-With (Mul (Map (FSin freqs) Zip-With (Suppress-Alias freqs amps)))
 Additive = Reduce(Add oscs)
FM(f0 ratio mod feedback)
 modulator = FSin(f0 * ratio) * mod
  carrier = FSin((1 + modulator + feedback * z-1('0 carrier)) * f0)
  FM = carrier
```



- Other clocks besides audio generators can be used
- Resonator coefficient computations are clocked from OSC
 - Only recomputes coefs when OSC signal arrives!

Listing 6: OSC-controlled Resonator Bank



 Classic Schroeder reverberation can be concisely expressed

Listing 7: Classical Schroeder Reverb

```
Feedback-for-RT60(rt60 delay)
{
   Feedback-for-RT60 = Crt:pow(#0.001 delay / rt60)
}

Basic(sig rt60)
{
   Use Algorithm
   allpass-params = ((0.7 #221) (0.7 #75))
   delay-times = (#1310 #1636 #1813 #1927)
   feedbacks = Map(Curry(Feedback-for-RT60 rt60) delay-times)

   comb-section = Reduce(Add Zip-With(Curry(Delay sig) feedbacks delay-times))
   Basic = Cascade(Allpass-Comb comb-section allpass-params)
```



· Some highlights from a FDN reverberator

Listing 8: Snippets of a 16th order FDN reverberator

```
/* Orthogonal matrix multiply - Householder Algorithm */
Feedback-Mtx(input)
{
Use Algorithm
Feedback-Mtx = input
(even odd) = Split(input)
even-mtx = Recur(even) odd-mtx = Recur(odd)
Feedback-Mtx = Append(Zip-With(Add even-mtx odd-mtx) Zip-With(Sub even-mtx odd-mtx))
}
/* 16-channel feedback signal recursively passed through a unit delay operator */
feedback-vector = z-1('(0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0)

Zip-With(Mul loss-coefs Zip-With(Filter:OnePole
Feedback-Mtx(delay-vector) filter-coefs)))
```



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